2457 DAC counts is about 1.5V
819 DAC counts is about 0.5V
addresses 0x1800 to 1C03 same as 18 to 1F
180 to 207 used for board to board communications.
Preset should be 1 or 2, depending on delta clock cycles and offset
io203 1pps
io204 pull-up
io205 pull-up - board bus sync signal
b
This module and all modules attached to this CPU may have to share one IRQ
for Rx and TX FIFOs.
The ISR for this routine should only turn off the interrupt and dispatch the fork level processing routine
which will look at all the FIFOs and figure out which receive ones need to be emptied, and which
transmit ones need to be topped off.