



Schematic representation of the voltage relationships of the input signals to the Analog Transient Waveform Digitizer (ATWD). The systematic pattern of pedestal offsets is an artifact of the variations in the manufacturing process as they affect each buffer-amplifier/comparator pair. The offset pattern will fluctuate from read-out to read-out due to kT noise effects. In a gross sense, the pattern should be identical for all four ATWD channels.